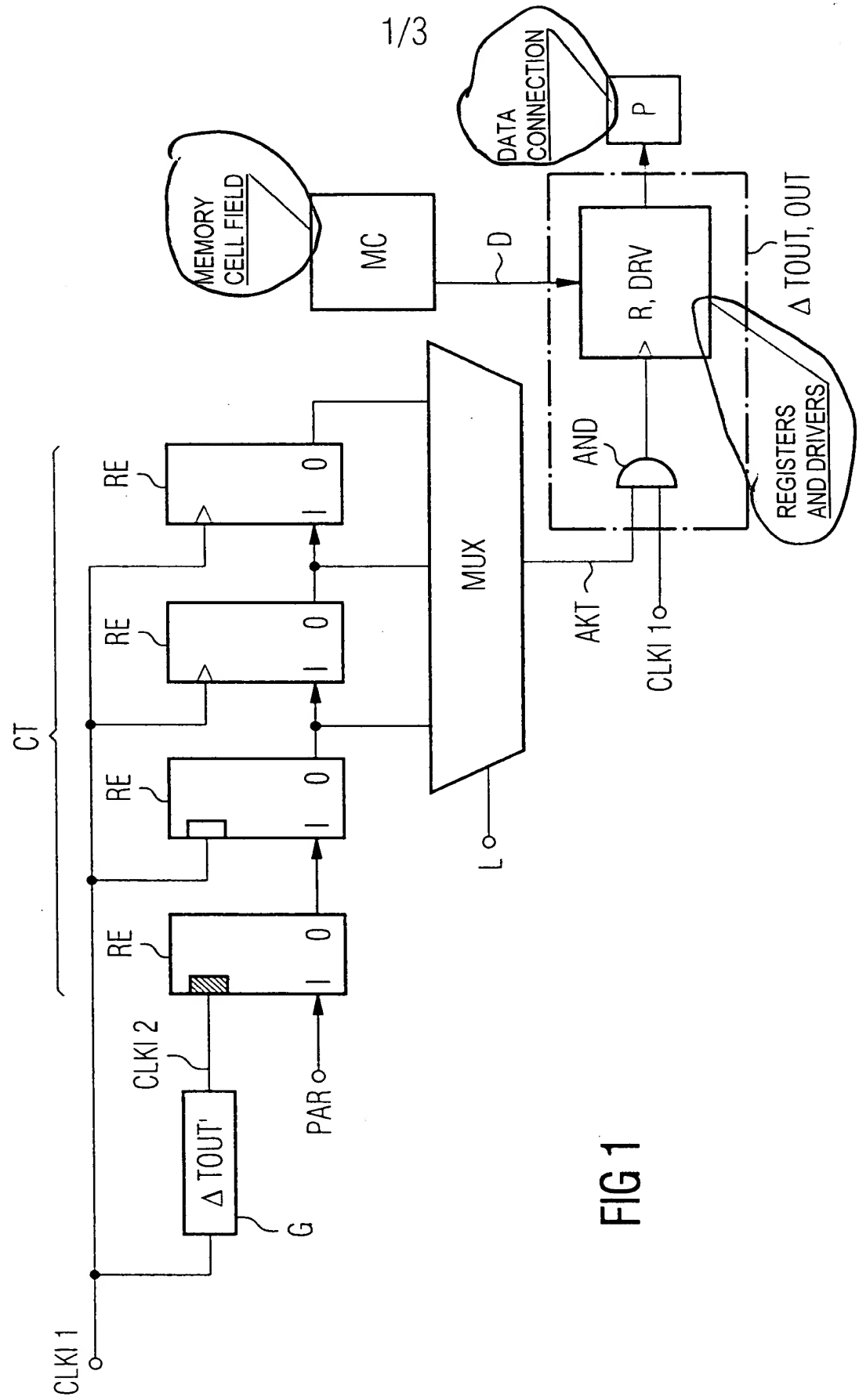
CLKI 1
CT
RE
RE
RE
RE
CLKI 2
Δ TOUT'
G
PAR
MUX
L
MEMORY CELL FIELD
MC
D
DATA CONNECTION
P
R, DRV
Δ TOUT, OUT
AND
AKT
CLKI 1
REGISTERS AND DRIVERS

FIG 1